

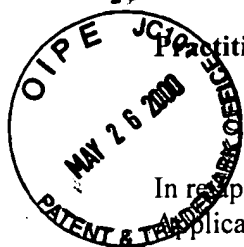
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**PATENT**

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Patitioner's Docket No. MI22-1322

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Agarwal, Vishnu K.

Application No.: 09/512,149

Filed: 02/23/2000

For: Integrated Circuitry and Method of Forming a Capacitor

Group Art No.:

1554

Examiner:

Unknown

Assistant Commissioner for Patents  
Washington, D.C. 20231

**REQUEST FOR CORRECTED FILING RECEIPT**

- Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
- There is an error with respect to the following, which is incorrectly entered.

**Error in**

**Correct data**

- Title
- Integrated Circuitry and Method of Forming a Capacitor
- The correction is not due to any error by applicant and no fee is due.

Dated: 5-23-00

Telephone No.: (509) 624-4276

Customer No.: 021567

By: 

Mark S. Matkin, Reg. No. 32,268  
WELLS, ST. JOHN, ROBERTS, GREGORY & MATKIN P.S.  
601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

**CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. 1.8a)**

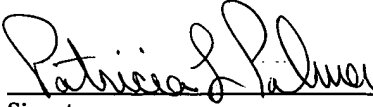
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Signature

Date: 5/23/00

Patricia L. Palmer  
(type or print name of person certifying)

MI22\1322\MO1.wpd

(Request for Corrected Filing Receipt—page 1 of 1)

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# 4



Bib Data Sheet

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Patent and Trademark Office**Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231

<b>SERIAL NUMBER</b> 09/512,149	<b>FILING DATE</b> 02/23/2000 <b>RULE</b> -	<b>CLASS</b> 257	<b>GROUP ART UNIT</b> 2811	<b>ATTORNEY DOCKET NO.</b> MI22-1322	
<b>APPLICANTS</b> Vishnu K Agarwal, Boise, ID ;					
** CONTINUING DATA <i>none, NDS</i> *****					
** FOREIGN APPLICATIONS <i>none, NDS</i> *****					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/24/2000</b> -					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		<b>STATE OR COUNTRY ID</b>	<b>SHEETS DRAWING</b> 5	<b>TOTAL CLAIMS</b> 55	<b>INDEPENDENT CLAIMS</b> 6
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met <i>Allowance</i>					
Verified and Acknowledged <i>Examiner's Signature</i> Initials					
<b>ADDRESS</b> 021567					
<b>TITLE</b> Integrated circuitry and method of forming a capacitor					
<b>FILING FEE RECEIVED</b> 1554	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		